

Tuning of a Feedforward Lag-Lead First-Order Compensator used with a Delayed Double Integrating Process

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Abstract:

The problem of controlling an unstable delayed double integrating process using a feedforward first-order lag-lead compensator is studied. The effect of time delay of the process in a range between 1 and 12 seconds is considered. The compensator is tuned using MATLAB optimization toolbox with five forms of the objective function in terms of the error between the step time response of the closed-loop control system and the response steady-state value. Using the proposed compensator with the delayed double integrating process indicates the robustness of the compensator in the time delay range used with superior time-based specifications compared with other techniques based on using PID controllers with the same process.

Keywords –*Feedforward first-order lag-lead compensator , Delayed double integrating process, Compensator tuning, Control system performance, Compensator robustness.*

I. INTRODUCTION

Delayed double integrating processes are examples of unstable processes which require extensive efforts in proper selection of suitable controllers or compensators and also looking for proper tuning techniques to achieve stable control system and accepted performance both in the time and frequency domains. Researchers handled this type of processes over the last decades. Some of the efforts in this aspect are presented here.

De Paor (2002) developed a procedure for the control of integrating and unstable processes with time delay. He presented examples including simple integrating and double integrating delayed processes [1]. Lin, He, Gu and Zhang (2004) proposed two control schemes to control dynamic plants with time delay and double integrators. They analysed the closed-loop robust stability and corresponding sufficient and necessary conditions in the presence of plant uncertainty [2]. Shamsuzzoha and Lee (2008) proposed a simple IMC-PID controller design technique for the first-order delay integrating process,

delayed integrating process and double integrating process. They focused on the disturbance rejection and used a 2DOF control structure to eliminate the set point response overshoot [3].

Wang and Zhang (2009) discussed the problem of determining the parameters sets of stabilizing PID controllers for first-order plus double integrating processes with time delay. They presented case studies to illustrate the design procedure showing the shapes of the stabilizing regions [4]. Zhou, Duan and Lin (2010) studied the dynamic problem of global stabilizing a double integrator system subject to actuator saturation and input delay. They illustrated the effectiveness of their approach through an example [5]. Vasickaninova and Bakosova (2011) studied the tuning of neuro-fuzzy controllers for integrating plant and for integrating plants with time delay. They compared with classical PID control [6]. Skogestad and Grimholt (2012) studied the optimality of the SIMC PI rules using IAE performance. They presented applications for first-order, second-order integrating with lag, integrating and double integrating delayed processes [7].

Thirunavukkarasu, Zyla, George and Priya (2013) discussed the tuning of a PID controller with time delay for double integrator systems for particular stability margins. They considered as a case study a double integrating process of 0.7 gain and 0.1 s time delay [8]. Hassaan (2014) used a first-order lag-lead feedforward compensator to control a simple pole plus an undelayed double integrator process. He used an ISE objective function incorporating the errors between the closed-loop system gain and phase margins and a desired levels of the two stability parameters. He compared his results with other published work [9]. Anil and Sree (2015) designed a PID controller for various forms of integrating systems with time delay using the direct synthesis method. They adjusted a tuning parameter to achieve a desired robustness, They provided tuning rules in terms of process parameters for various forms of integrating systems [10]. Hassaan (2015) used a first-order lag-lead compensator to control a very slow

second-order process of 150 s settling time. He tuned the compensator using an ISE objective function and could reduce the settling time to only 0.666 s [11].

II. PROCESS

The controlled process is delayed double integrating process having the transfer function, $G_p(s)$:

$$G_p(s) = (K_p/s^2) \exp(-T_d s) \quad (1)$$

Where K_p is the process gain and T_d is its time delay. It is dealt with the exponential term in Eq.1 through the first-order Taylor series as [7]:

$$\exp(-T_d s) \approx 1 - T_d s \quad (2)$$

Combining Eqs.1 and 2 gives the process transfer function as:

$$G_p(s) = (-K_p T_d s + K_p) / s^2 \quad (3)$$

The unit step response of the process using Eq.3 is shown in Fig.1.

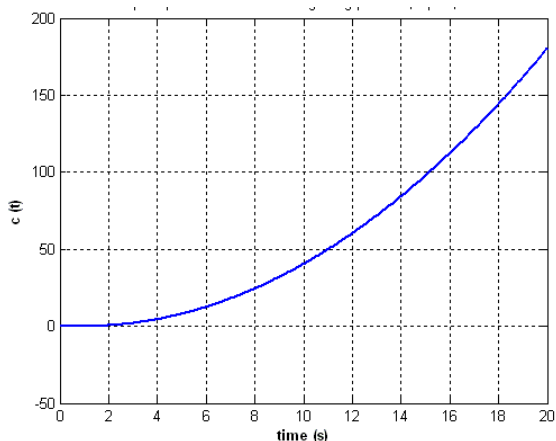


Fig.1 Step response of the double integrating process.

It is clear from Fig.1 that the double integrating process is an unstable 1. The compensator has to generate an stable feedback control system and also to achieve an accepted performance through tuning the compensator.

III. COMPENSATOR

The compensator used is a feedforward first-order compensator having the transfer function, $G_c(s)$ [9, 11, 12]:

$$G_c(s) = K_c(1 + T_z s) / (1 + T_p s) \quad (4)$$

Where K_c is the compensator gain, T_z and T_p are two time constants of the compensator.

IV. CONTROL SYSTEM TRANSFER FUNCTIONS

For a unit feedback control system, the open-loop transfer function of the control system incorporating the feedforward first-order compensator and the delayed double integrating process, $G(s)H(s)$ is given by:

$$G(s)H(s) = (b_0 s^2 + b_1 s + b_2) / (T_p s^3 + s^2) \quad (5)$$

Where:

$$\begin{aligned} b_0 &= -K_c K_p T_z T_d \\ b_1 &= K_c K_p T_z - K_c K_p T_d \\ b_2 &= K_c K_p \end{aligned}$$

The closed-loop transfer function of the control system, $M(s)$ is given for a unit feedback elements by:

$$M(s) = (b_0 s^2 + b_1 s + b_2) / [T_p s^3 + (1+b_0)s^2 + b_1 s + b_2] \quad (6)$$

V. COMPENSATOR TUNING AND SYSTEM TIME RESPONSE

The compensator has to be tuned to achieve two purposes:

- (i) Providing a stable closed-loop control system.
- (ii) Controlling the performance of the closed-loop control system in terms of time-based and frequency based specifications.

The compensator is relatively simple having only three parameters: K_c , T_z and T_p . The compensator parameters are tuned as follows:

- The optimization toolbox of MATLAB is used for this purpose [13].
- The MATLAB command '*fminunc*' is used [13].
- A number of objective functions based on the error between the step time response of the control system and its steady-state response are selected to tune the compensators. They are ITAE, ISE, IAE, ITSE and ISTSE [14-17].
- The tuning procedure is applied for a specific time delay of the double integrating process in the range $1 \leq T_d \leq 12$ s.
- The step response of the closed-loop control system is plotted using the command '*step*' of MATLAB [18].
- The time-based specifications of the control system are extracted using the MATLAB command '*stepinfo*' [18].
- The frequency-based specifications of the control system are extracted using the MATLAB command '*margin*' [18].

A sample of the tuning results is shown in Table 1 for a unit time delay of the double integrating process and a unit gain.

Table 1: Compensator tuning for process unit gain and unit time delay.

Objective Function	K_c	T_z	T_p
ITAE	3.2932	-1	4.1716
ISE	3.2919	-1	4.1707
IAE	3.2919	-1	4.1707
ITSE	3.2925	-1	4.1712
ISTSE	3.2932	-1	4.1716

The time response of the control system for a unit step input is shown in Figs.2 for time delay of 6 s

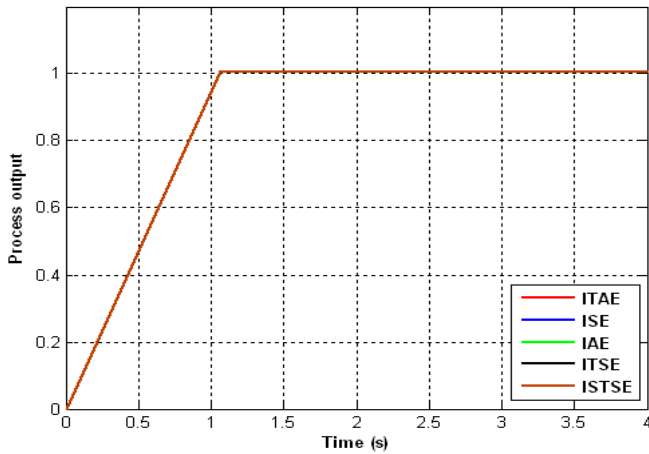


Fig.2 Control system time response for a 6 s time delayed double integrating process.

Varying the type of the optimization objective function does not affect the time response of the control system. This means that all of them have the same weight in tuning the first-order compensator used with this delayed double integrating process.

The effect of the time delay of the double integrating process on some of the time-based specifications of the control system is shown in Fig.3.

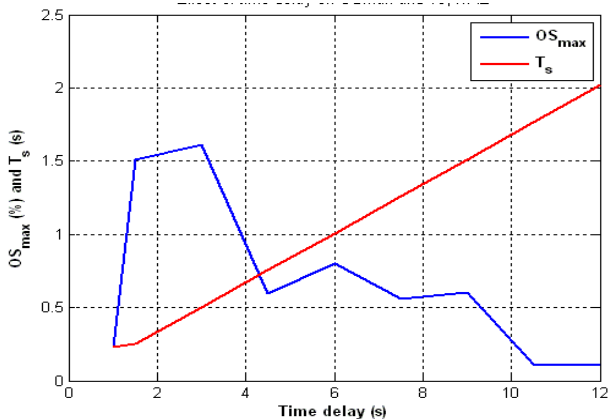


Fig.3 Effect of process time delay on overshoot and settling time.

The settling time increases almost linearly as the time delay increases. The maximum percentage overshoot changes between 0.11 and 1.61 % during the time delay range between 1 and 12 s.

VI. COMPARISON WITH OTHER RESEARCH WORK

The unit time response of the control systems as presented in the present work using a first-order feedforward compensator is compared with the work of Skogestad and Grimholt [7] and Anil and Sree [10] using tuned PID control to control the delayed double integrating process. The comparison is presented graphically in Fig.4.

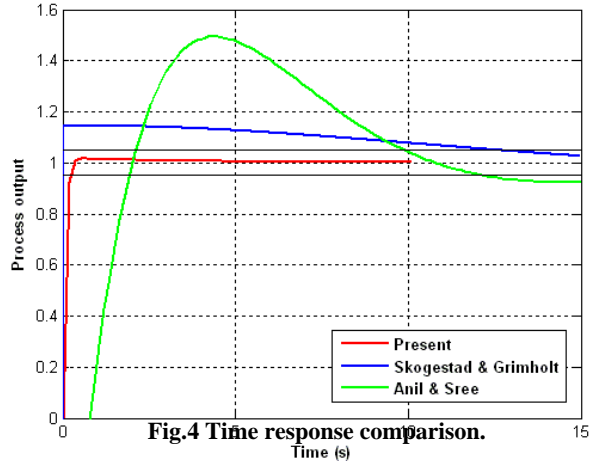


Fig.4 Time response comparison.

The present work gives outstanding time response compared with the other techniques. The compared work is for a process having unit gain and unit time delay.

VII. CONCLUSION

- The dynamic problem of tuning a feedforward lag-lead compensator for use with a delayed double integrating process was investigated.
- The effect of the process time delay on the tuning results and the time response of the control system was investigated.
- Five objective function forms were applied to tune the compensator.
- It has been shown that all the applied objective function forms have led to almost the same tuning parameters of the compensator and to the same time-based specifications.
- The proposed compensator succeeded to produce a time response to a step reference input having a maximum percentage overshoot as low as 0.11 % at a specific time delay value.
- Corresponding to the time delay range covered in the analysis, the maximum percentage overshoot did not exceed 1.61 %.
- The settling time of the control system step response did not exceed 2 seconds during the covered time delay range.
- The feedforward first-order lag-lead compensator was robust against the change

in the process time delay in the range between 1 and 12 seconds.

- Although the studied delayed double integrating process was an unstable one, the proposed compensator has given superior results when compared with other research works based on using PID controllers.
- For a process of unit gain and unit time delay, the maximum percentage overshoot was 1.542 % for the present work compared with 14.42 % for the Skogestad and Grimholt work and 49.44 % for the Anil and Sree work.
- the settling time was 0.231 second for the present work compared with 12.50 seconds for the Skogestad and Grimholt work and 9.09 seconds for the Anil and Sree work.

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